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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
GROUP 230

Applicant: Gordon E. Morrison et al

Group Art Unit: 237 ✓

Serial No. 560,093

Examiner: E. Chan

Filed: July 30, 1990

Title: Parallel Processing Method and Apparatus  
for Increasing Processing Throughput by  
Parallel Processing Low Level Instructions  
Having Natural Concurrency

Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Sir:

SECOND PRELIMINARY AMENDMENT

Prior to examination of the application, please add the  
following claims:

In the Claims

Add claims 78-83 as follows:

78. A method for executing branches in single entry-single  
exit (SESE) basic blocks (BBs) contained within a program, said  
method comprising the steps of:

determining the branch instruction within each said basic  
block of said program,

adding information to said branch instruction,  
processing said instructions in each said basic block, and  
completing the execution of said branch instruction in said  
basic block no later than during the time duration of processing  
the last instruction in said basic block so that the execution of